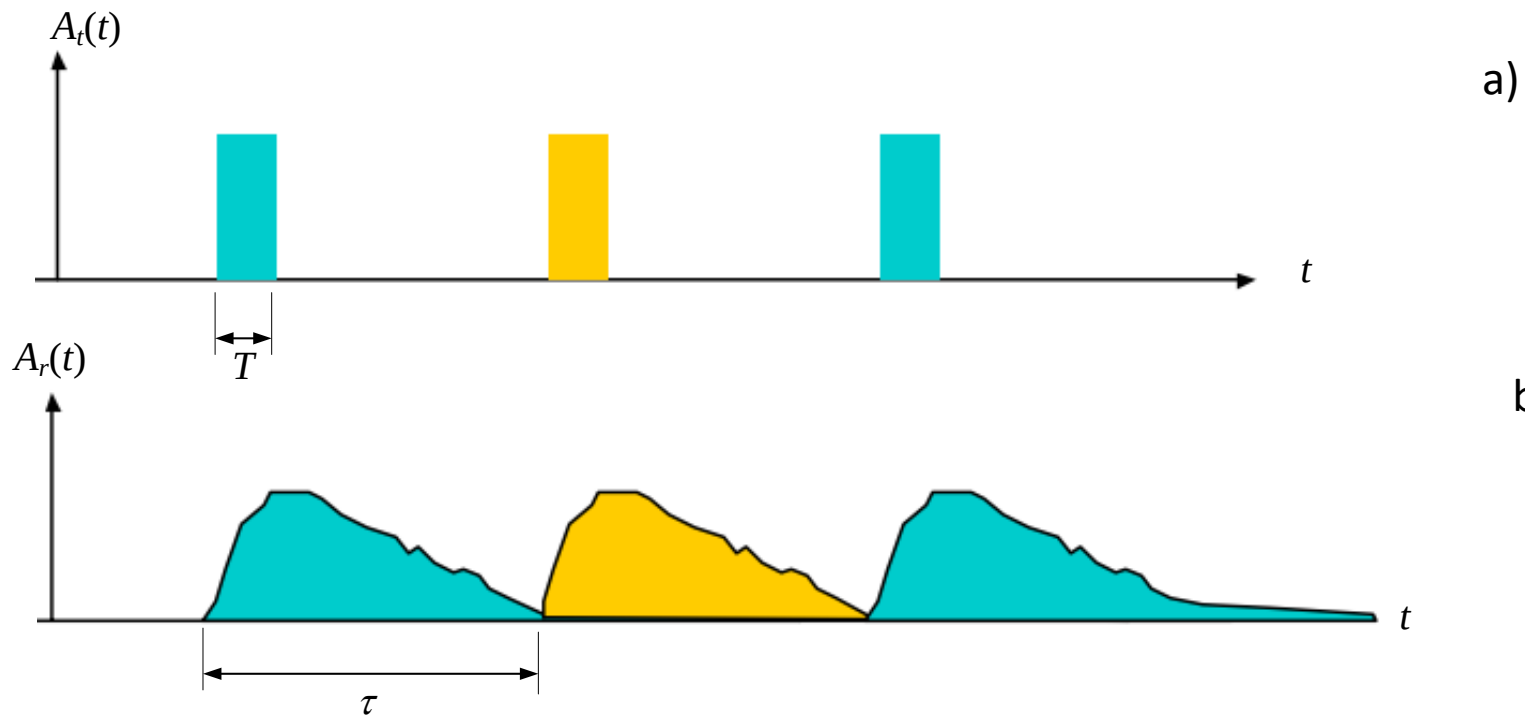
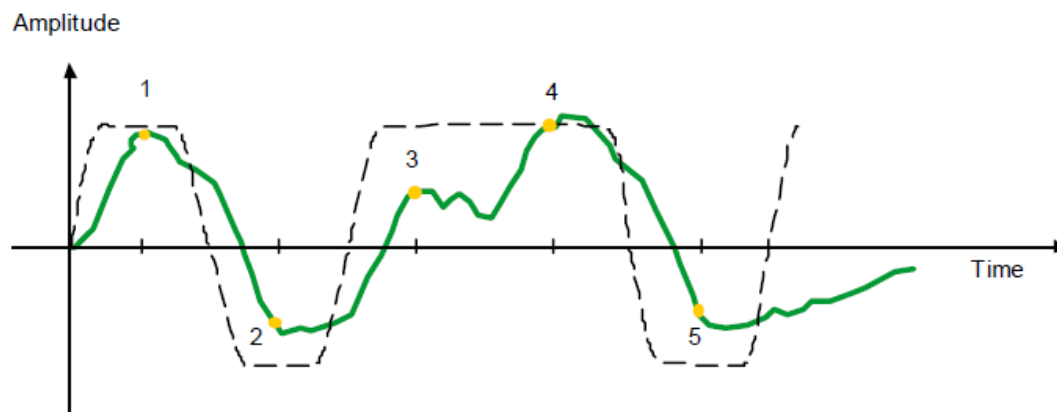
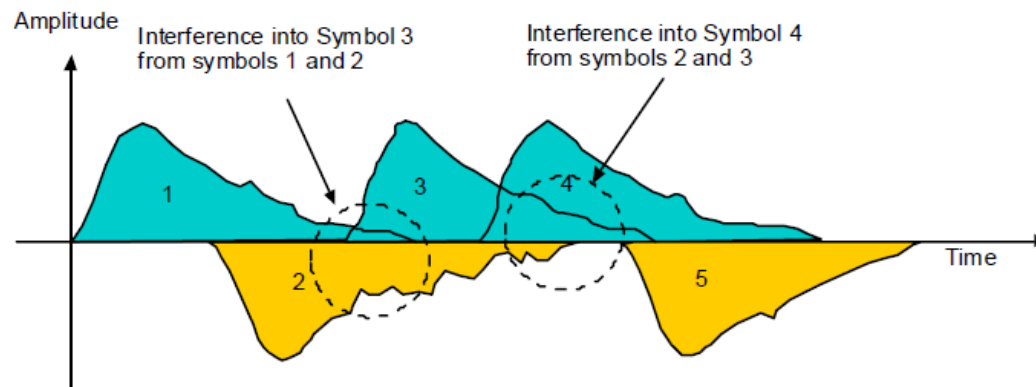
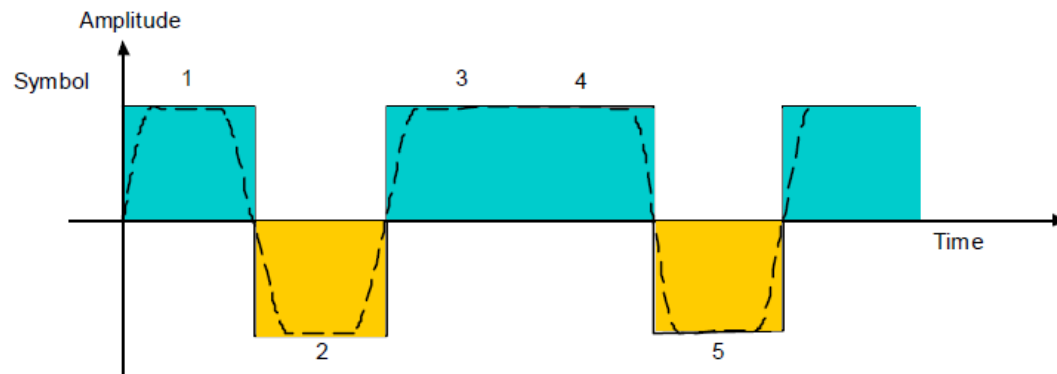
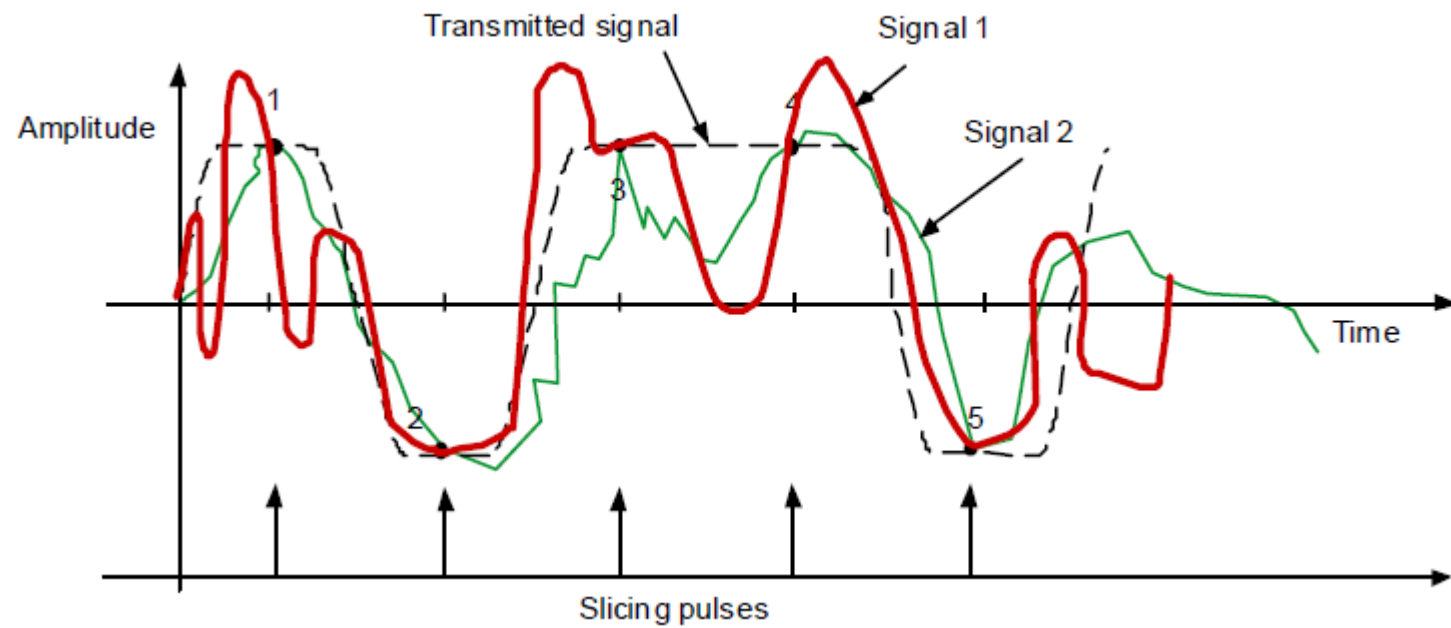
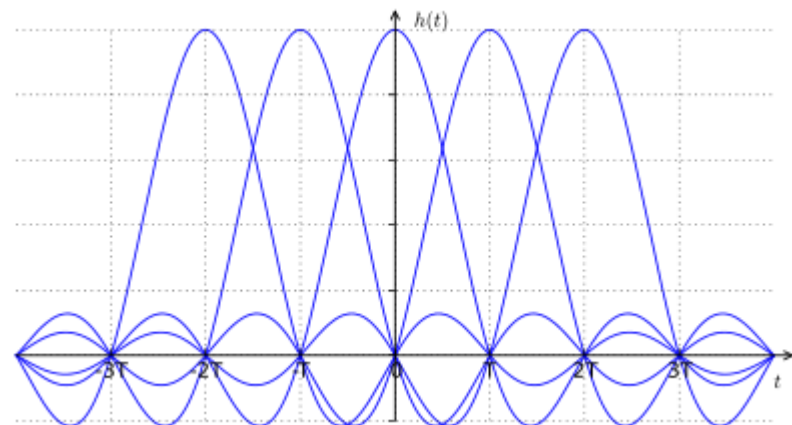
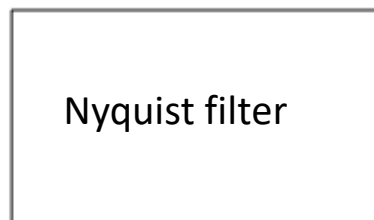
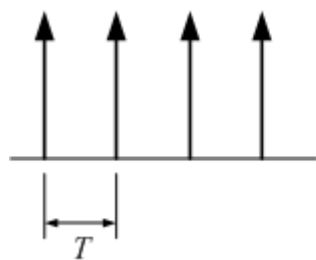




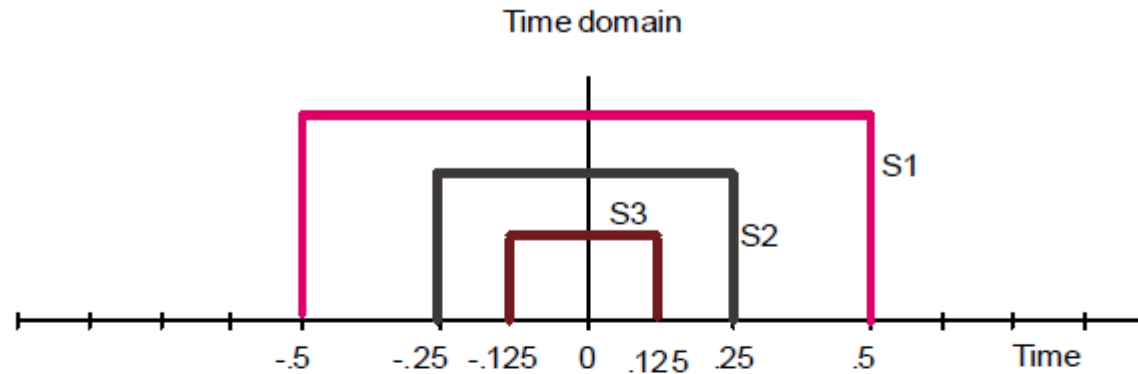




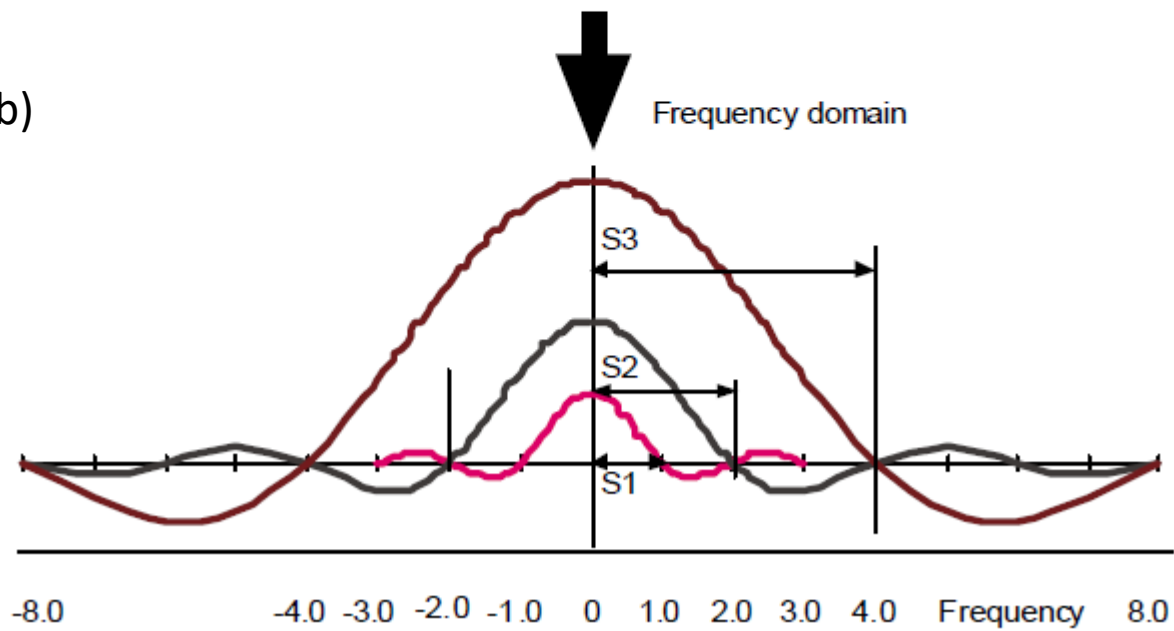


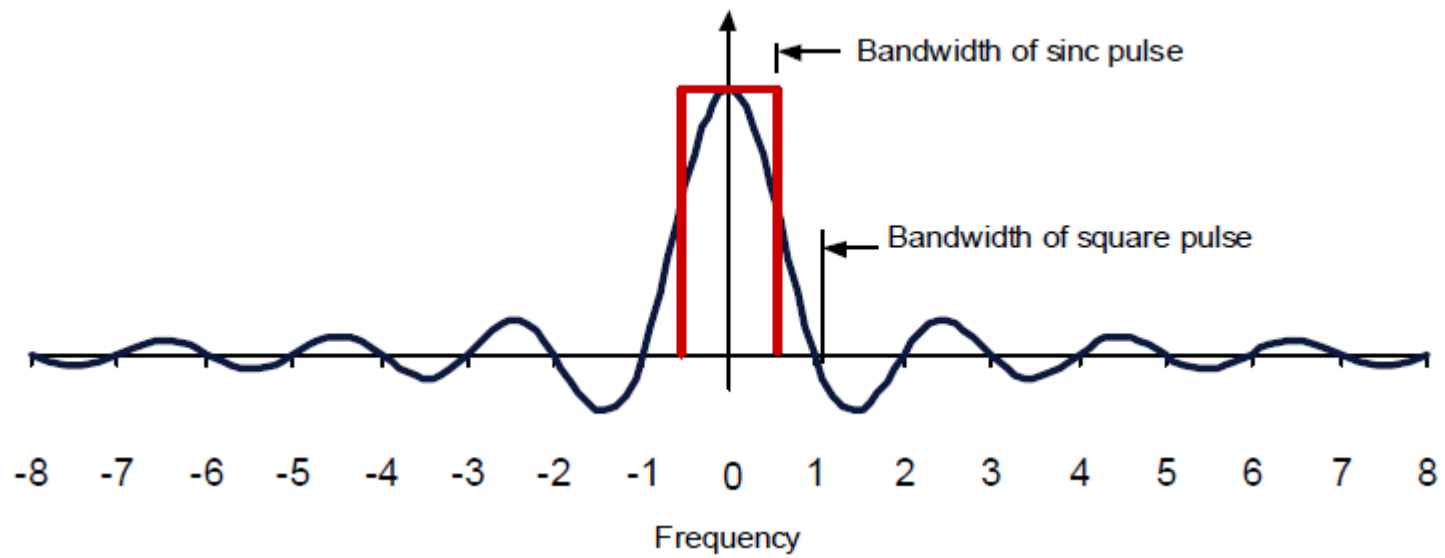
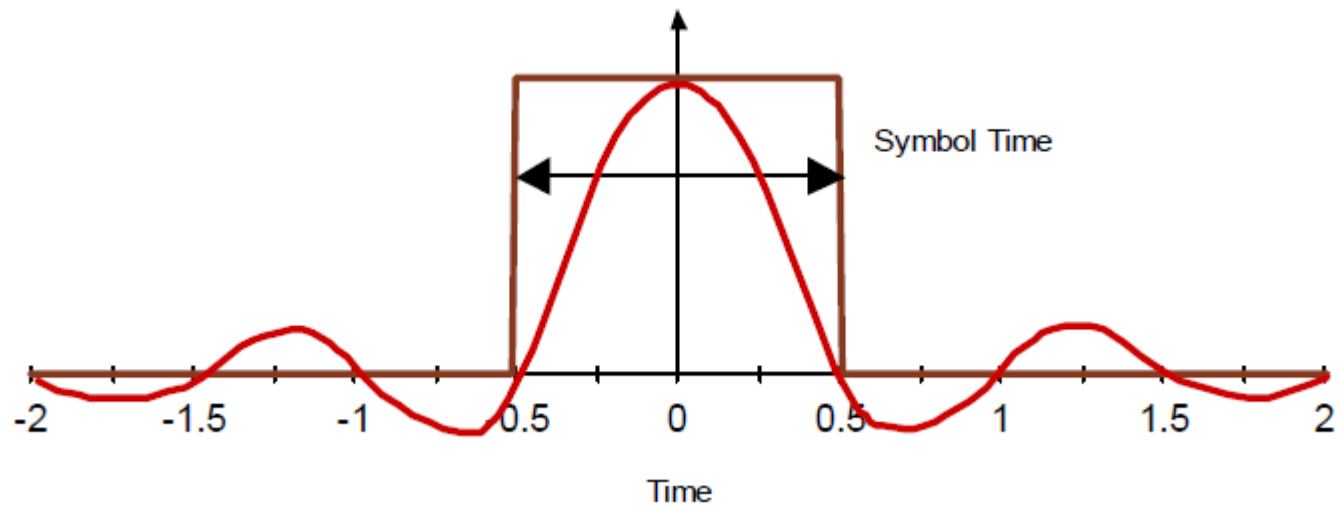
no ISI

a)



b)





RCF:

$\cos$ part significantly reduces the decay of the signal amplitude.

$$h(t) = \frac{\sin(\pi t / T_s)}{\pi t / T_s} \frac{\cos(\pi \rho t)}{1 - (2\rho t / T_s)^2}$$

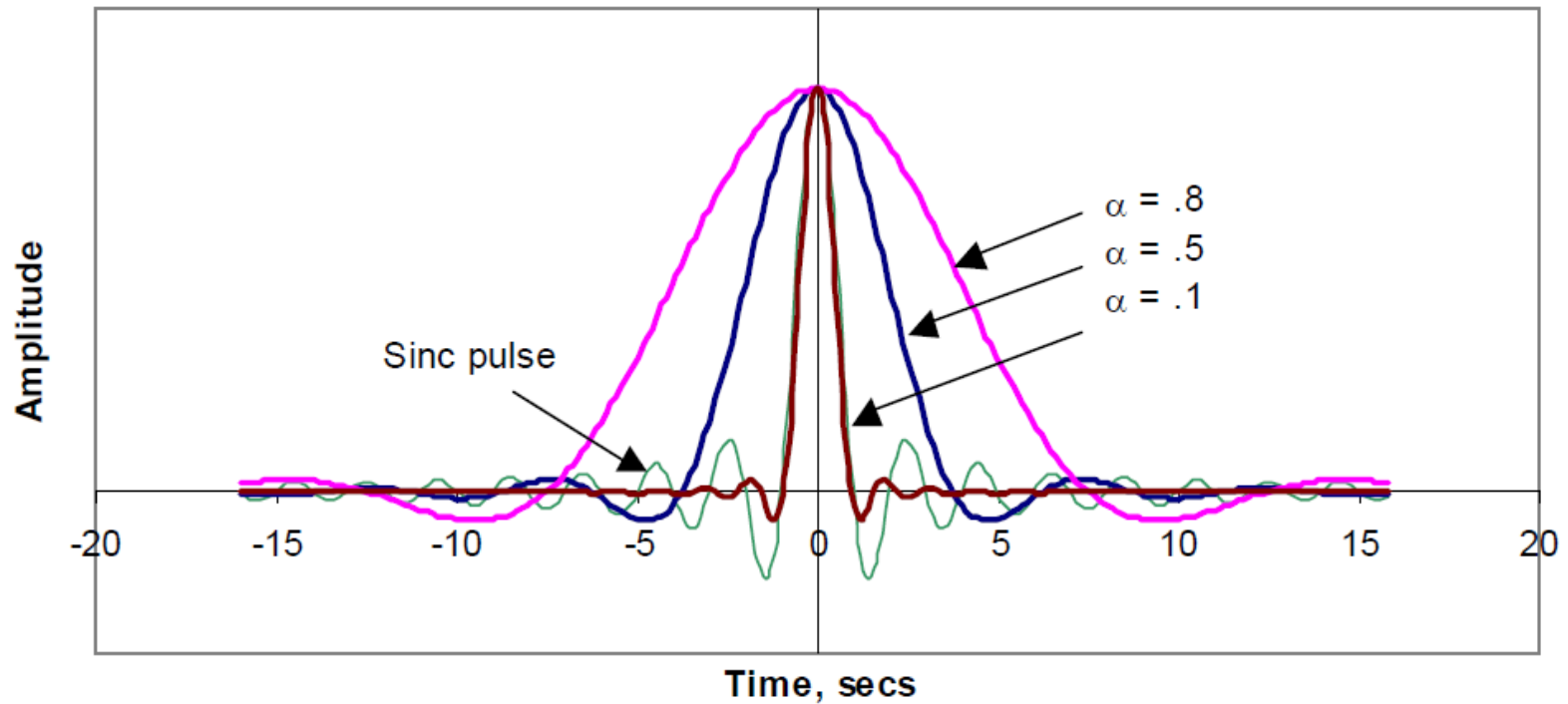
sinc pulse ensures zero crossings in integer multiples of the modulation rate (the advantage is easy recovery of the clock signal (synchronization) on the receiver side)

$$H(f) = \begin{cases} 1 \\ \cos^2 \left\{ \frac{\pi}{4} \frac{|f| + W - 2W_0}{W - W_0} \right\} \\ 0 \end{cases}$$

$$|f| < 2W_0 - W$$

$$2W_0 - W < |f| < W$$

$$|f| > W$$



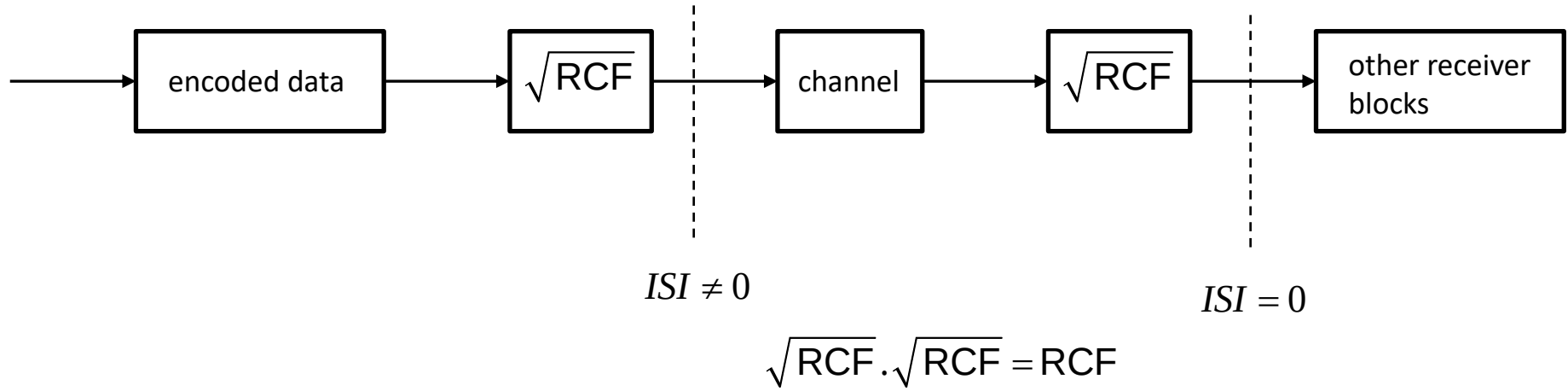
Roll – off factor (is denoted also α) and is defined for baseband: $\rho = \frac{W - W_0}{W_0}$ $\rho \in \langle 0, 1 \rangle$

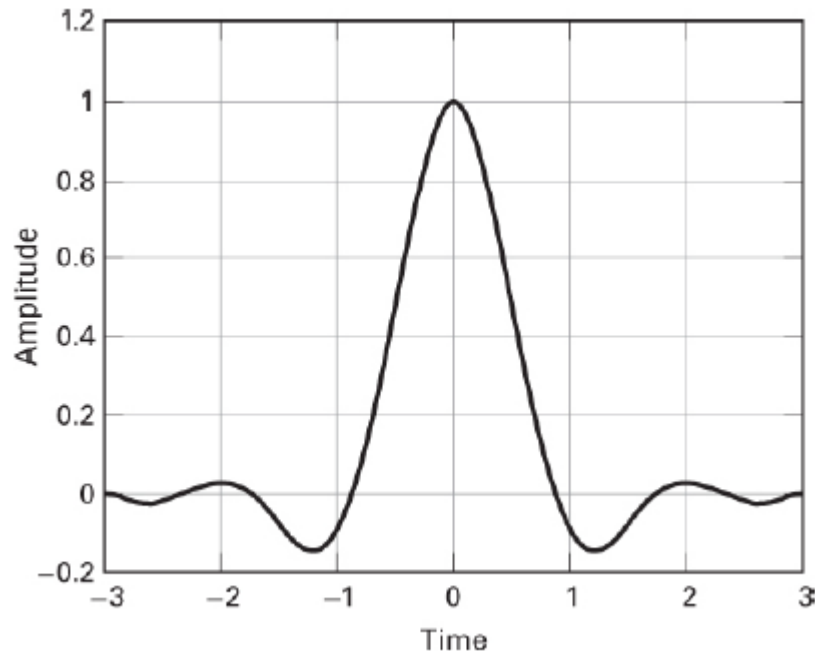
Roll – off factor enables to define a real system bandwidth W :

BB: $W = \frac{1}{2}(1 + \rho)R_s$

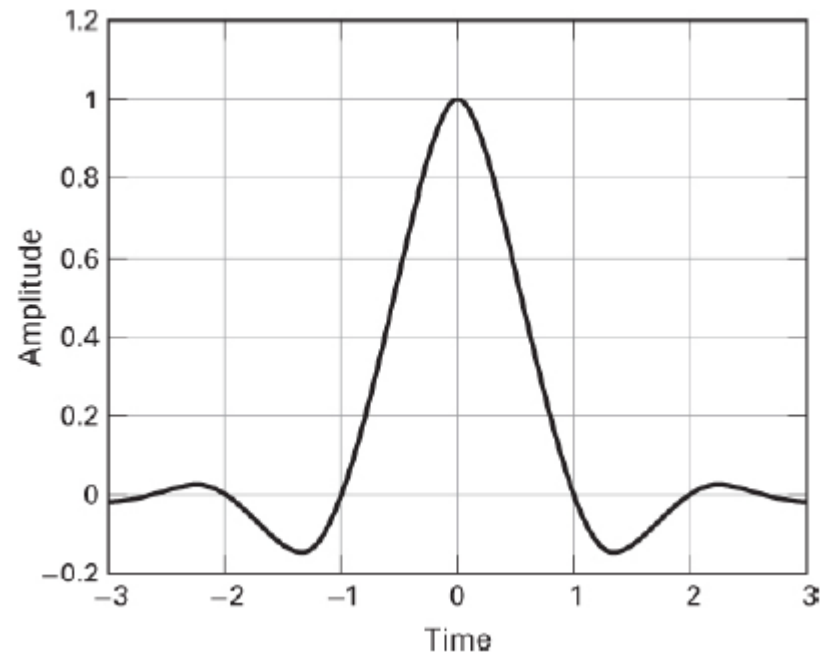
BP: $W = (1 + \rho)R_s$

RRCF realization





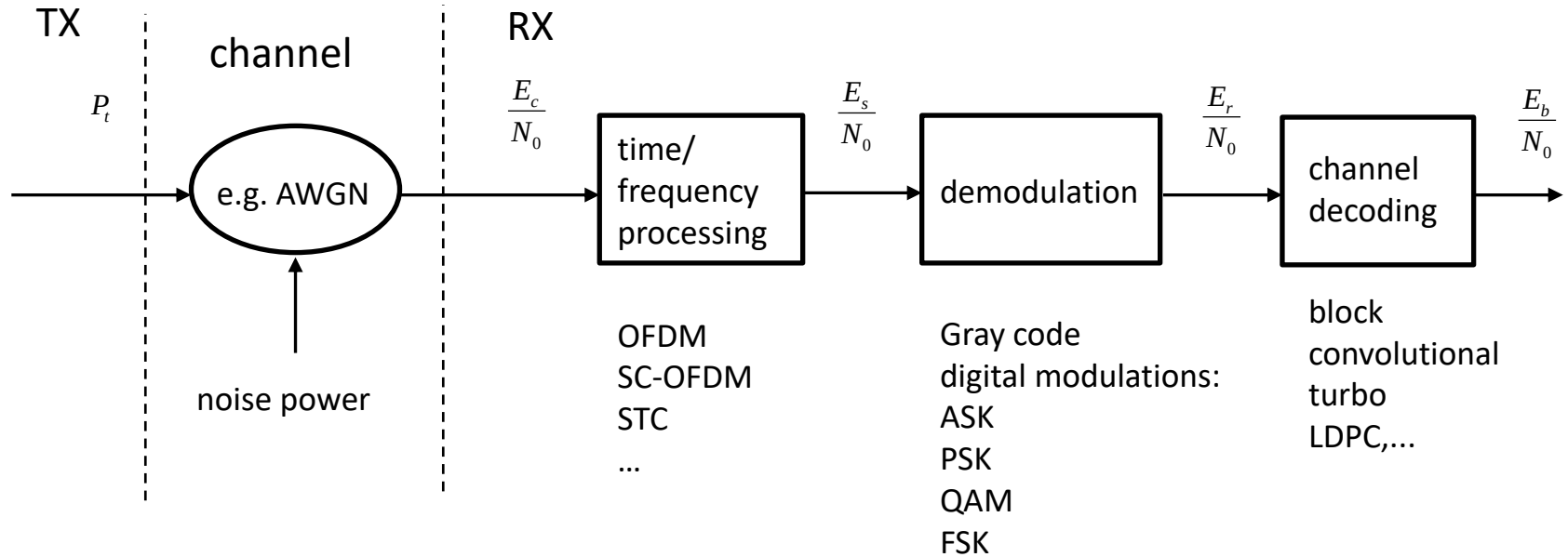
RRCF pulse



RCF pulse

source: B. Sklar, F. Harris: Digital Communications

SNR



$$\frac{E_x}{N_0}$$

- normalized SNR at:

bit level: $\frac{E_b}{N_0}$

code level: $\frac{E_r}{N_0}$

symbol level: $\frac{E_s}{N_0}$

chip level: $\frac{E_c}{N_0}$